

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10046893	FILING DATE 01/17/2002	CLASS 438	SUBCLASS 149	GAU 2812	EXAMINER H. Lee
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<b>**CONTINUING DATA VERIFIED:</b> Lee NOTE					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> Lee JAPAN 2001-010890 01/18/2001 JAPAN 2001-019357 01/29/2001 Yes JAPAN 2001-022398 01/30/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials Lee				740756-2424	
TITLE : Method of manufacturing semiconductor device					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawn	Flgs. Drawn
		Print Flg	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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